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	III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010 COMPUTER ORGANIZATION (COMMON TO EEE, EIE, ECE, ETM, ICE) Time: 3hours Max.Marks:80 Answer any FIVE questions All questions carry equal marks	
1.a) b)	Compare dedicated and multiplexed bus lines. Discuss various bus arbitration techniques. [8+3	8]
2.	Convert the following decimal numbers to base three and base five. i) 75 ii) 13.245 iii) 25.21	6]
3.	Explain common addressing modes with examples. [16	5]
4.	Explain instruction cycle in detail. [16	5]
5.	Explain different techniques of cache mapping functions and specify merits and demeri of each. [16	ts 5]
6.a) b)	Explain associative memory organization.What is demand paging? Explain merits and demerits of it.[8+3]	8]
7.a) b) c)	Give the general-description of on I/O module structure. Discuss about possible DMA configurations. What is interrupt service Routine? Explain. [10]	5]
8.a) b)	Classify and explain different multiprocessors. How branch instruction is handled in pipelined processors? Explain. [8+3]	8]

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Code.	No: RR310201	RR		SET-4			
III B.TECH – I SEM EXAMINATIONS, NOVEMBER - 2010 COMPUTER ORGANIZATION (COMMON TO EEE, EIE, ECE, ETM, ICE) Time: 3hours Max.Marks:80 Answer any FIVE questions							
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